

					
<p align="center">U.S. UTILITY Patent Application</p>					
<p>APPLICANT</p>		<p>CLASS</p>		<p>ART UNIT</p>	
<p>Bulent Basol Cyprian Uzoh Homayoun Talieh</p>		<p>451</p>		<p>3723</p>	
<p>CONT/PRIOR</p>		<p>SUBCLASS</p>		<p>EXAMINER</p>	
<p>D</p>		<p>4L</p>		<p>BAKES</p>	
<p>TITLE</p>					
<p>Process to minimize and/or eliminate conductive material coating over the top surface of a patterned substrate and layer structure made thereby</p>					
<p align="right">PTO-2040 12/89</p>					

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	

Form PTO-436A
(Rev. 6/99)